

Review of PWM Filters for AC Voltage Source Converters

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AGENDA

- 1. Applications/Challenges of Passive Filters
- 2. Characterization of Passive Filters
- 3. Damping Methods and Loss Optimization
- 4. Characterization of Inductive Components
- 5. Design Examples
- 6. Conclusions
- 7. Questions



1. Applications/Challenges of Passive Filters

A Voltage-Source converter (VSC) is the enabling technology of efficient power-electronics [1] in:

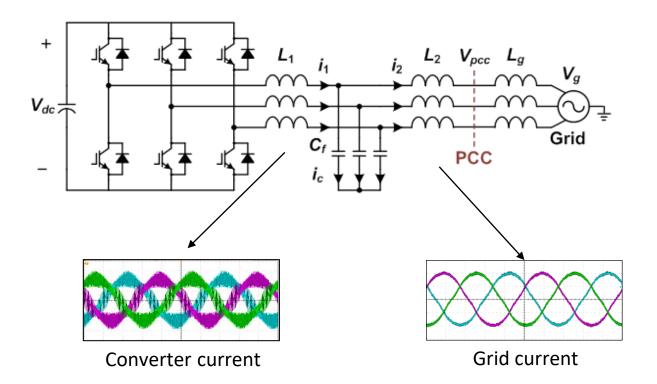
- Drives
- UPS and power conditioning
- Battery storage
- EV stations
- Air craft & marine power systems
- Railway electrification
- HVDC power system

[1] F. Blaabjerg, Z. Chen, and S. B. Kjaer, "Power Electronics as Efficient Interface in Dispersed Power Generation Systems," IEEE Trans. Power Electron., vol. 19, no. 5, pp. 1184–1194, Sep. 2004.



1. Applications/Challenges of Passive Filters

- High-order passive filtering is needed on the AC-side of the filter for size/cost considerations
- The LCL filter is widely adopted by industry [2]



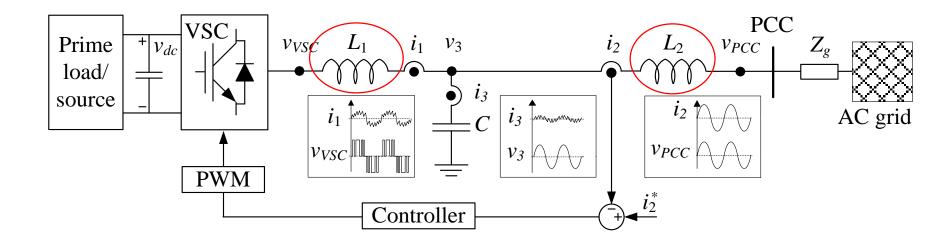
[2] M. Liserre, F. Blaabjerg, and S. Hansen, "Design and Control of an LCLFilter-Based Three-Phase Active Rectifier," IEEE Trans. Ind. Appl., vol. 41, no. 5, pp. 1281–1291, Sep. 2005.



1. Applications/Challenges of Passive Filters

Main challenges for passive filter design:

- Design of inductive components [3]
- Minimize resonance interaction between filter, control and grid



[3] R. Beres, H. Matsumori, T. Shimizu, X. Wang, F. Blaabjerg and C. L. Bak, "Evaluation of Core Loss in Magnetic Materials Employed in Utility Grid AC Filters," in Proc. of the 31st Annual IEEE Applied Power Electronics Conference and Exposition, APEC 2016, pp. 3051-3057.



Filter admittance characterization:

VVSC

• Filter input admittance (20 dB/dec):

$$Y_{11}(s) = \frac{i_1}{V_1}\Big|_{V_2=0}$$

Filter output admittance (20 dB/dec):

 v_g

$$Y_{22}(s) = \frac{-i_2}{V_2}\Big|_{V_1=0}$$

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• Filter transfer admittance (20 dB/dec at LF and 40~60 dB/dec at HF):

 $Y_{21}(s) = \frac{i_2}{V_1}$



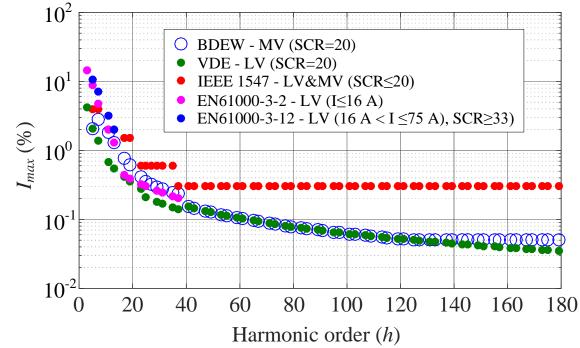
Harmonic standards and attenuation requirements

Individual current harmonic limits:

- 0.3 % HF limit specified by IEEE 1547
- 0.05 % HF limit specified by BDEW

Total harmonic distortion of the current:

• Less than ~5 %

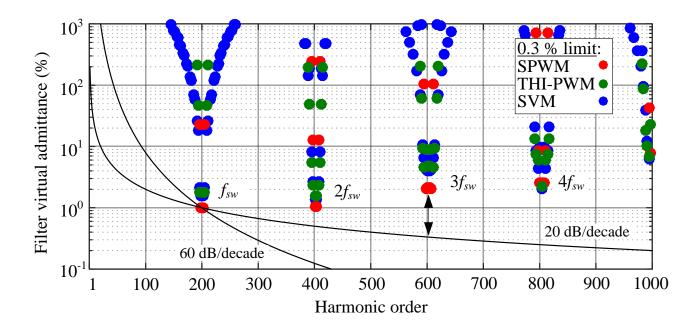




Influence of the PWM method and filter topology

Virtual harmonic filter admittance:

$$Y_{vhf} = \frac{i_{\text{limit}}(h)}{v_{VSC}(h)}$$



Selecting $Y_{21} \leq Y_{vhf}$ suffices performance criteria of passive filters!

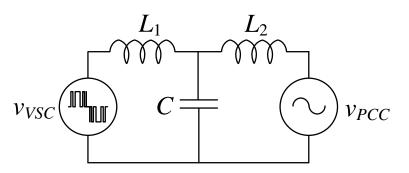


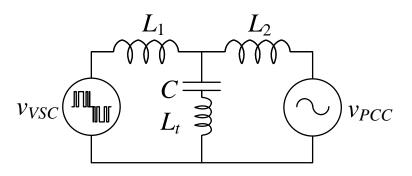
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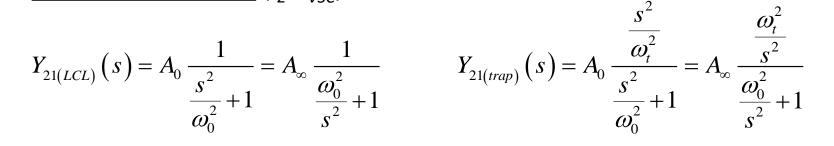
Alternative to LCL filter

LCL filter (3rd order low-pass) LLCL filter (LCL + trap filter)





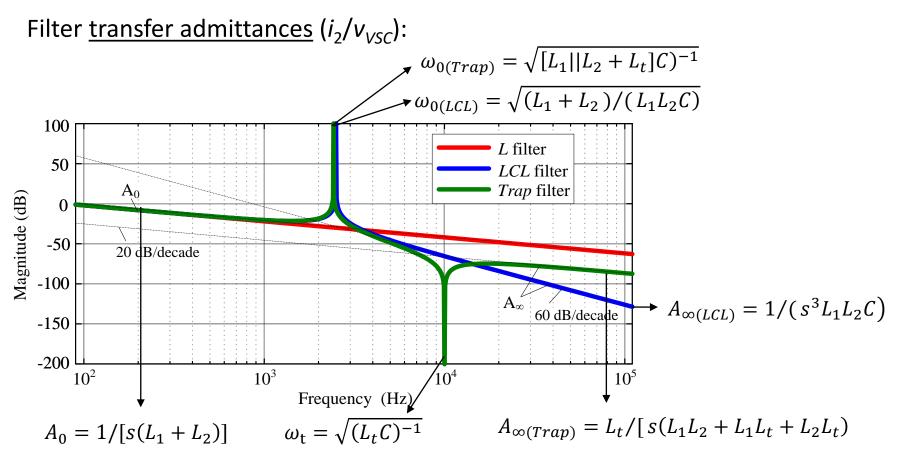
Filter <u>transfer admittances</u> (i_2/v_{VSC}) :



Selecting $Y_{21} \leq Y_{vhf}$ suffices performance criteria of passive filters!



Attenuation characteristics

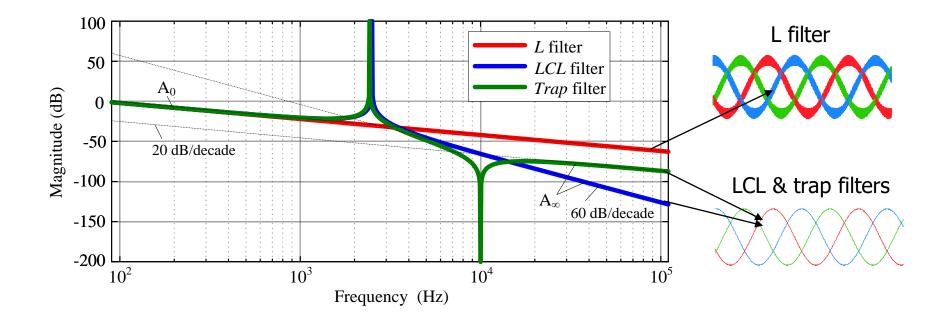


Selecting $Y_{21} \leq Y_{vhf}$ suffices performance criteria of passive filters!



Attenuation characteristics

Filter transfer admittances (i_2/v_{VSC}) :



Selecting $Y_{21} \leq Y_{vhf}$ suffices performance criteria of passive filters!

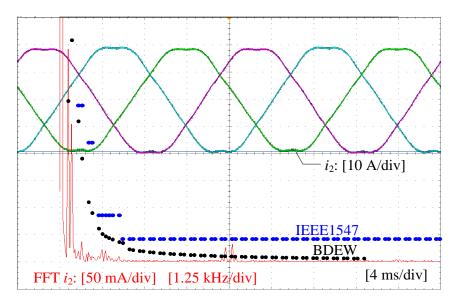


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Conventional passive damping method

Experimental waveforms – conservative design, fsw=5 kHz [4]

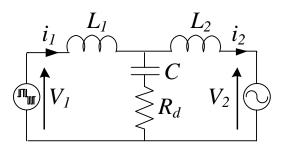


$$L_1 = 3.5 \text{ mH } (7 \%), L_2 = 1.5 \text{ mH } (3 \%),$$

$$C = 9.5 \mu \text{F} (5 \%), R_d = 1.4 \Omega (0.13 \%),$$

$$R_d = 0.03 \%$$

$$k_p = 8.5, k_i = 450$$

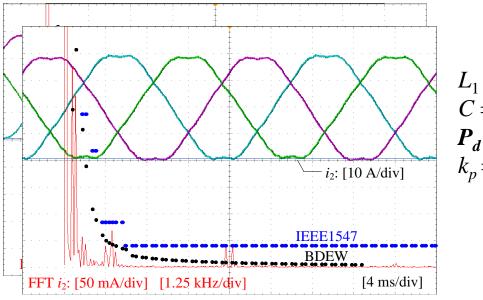


[4] R. Beres, X. Wang, M. Liserre, F. Blaabjerg, and C. L. Bak, "A Review of Passive Power Filters for Three Phase Grid Connected Voltage-Source Converters," IEEE Journal of Emerging and Selected Topics in Power Electronics, vol. 4, no. 1, 2016, pp. 54–69.



Conventional passive damping method

Experimental waveforms – optimal design, fsw=5 kHz [4]



$$L_1 = 2 \text{ mH} (4 \%), L_2 = 1.5 \text{ mH} (3 \%)$$

 $C = 20 \ \mu\text{F} (10 \%), R_d = 1.6 \ \Omega (0.3 \%),$
 $P_d = 0.4 \%,$
 $E_p = 5, k_i = 250$

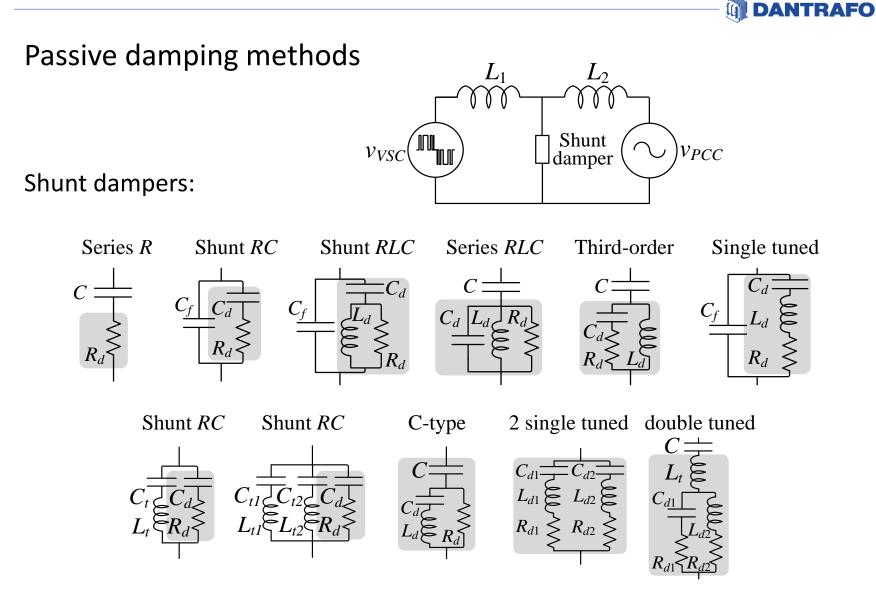
Filter size/cost is reduced with 30 %! Damping losses increases by a factor of 13!

[4] R. Beres, X. Wang, M. Liserre, F. Blaabjerg, and C. L. Bak, "A Review of Passive Power Filters for Three Phase Grid Connected Voltage-Source Converters," IEEE Journal of Emerging and Selected Topics in Power Electronics, vol. 4, no. 1, 2016, pp. 54–69.



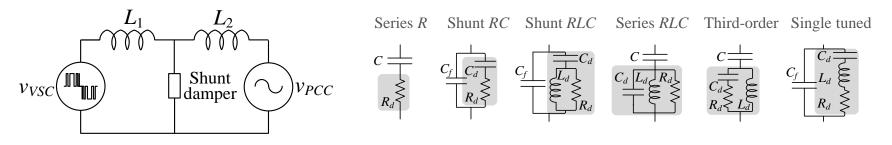
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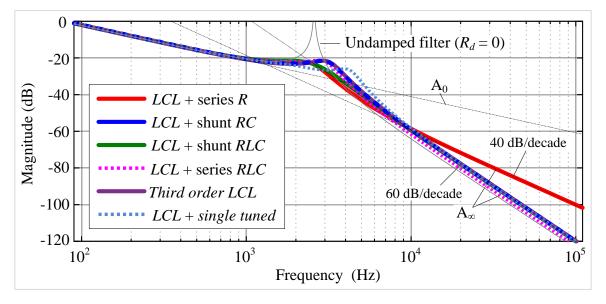




Shunt passive damped filters for LCL filter



Filter transfer admittance:



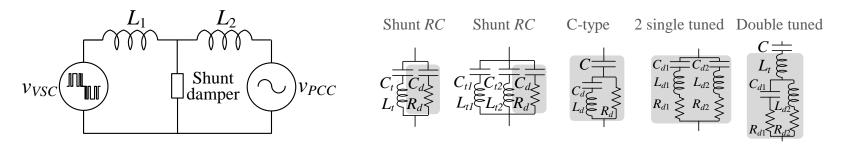
Mainly the resonance frequencies are affected!



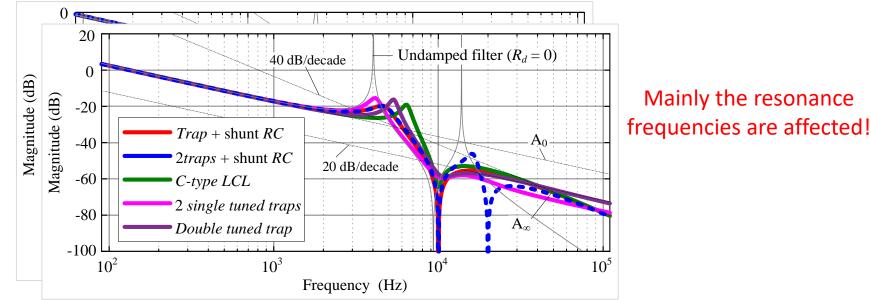
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Shunt passive damped filters for Trap filter



Filter transfer admittance:

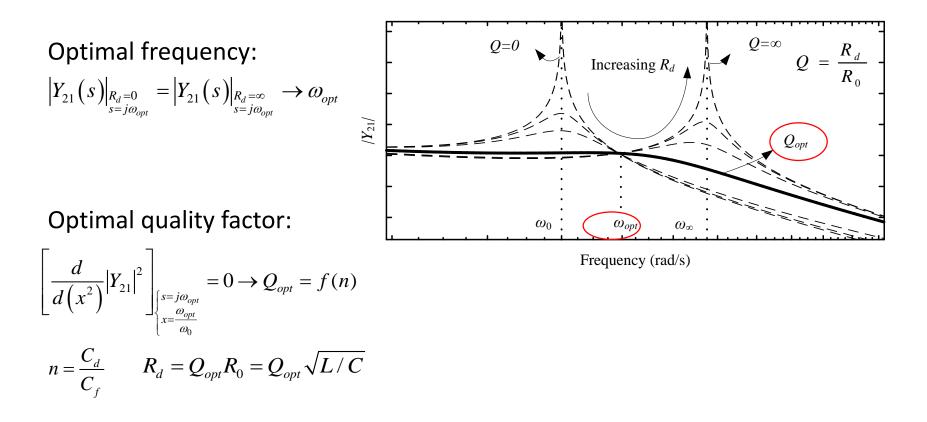




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Shunt passive damped filters – optimal design concept [5]

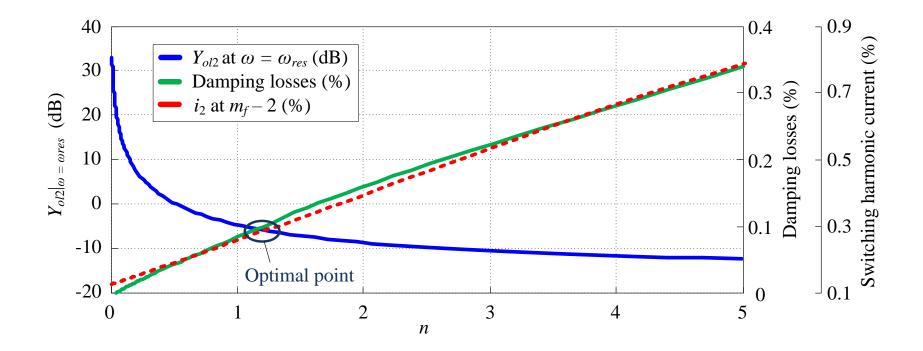


[5] R. Beres, X. Wang, F. Blaabjerg, M. Liserre, and C. L. Bak, "Optimal Design of High-Order Passive-Damped Filters for Grid-Connected Applications," IEEE Trans. Power Electron., vol. 31, no. 3, 2016, pp. 2083–2098.



Optimal selection of filter parameters

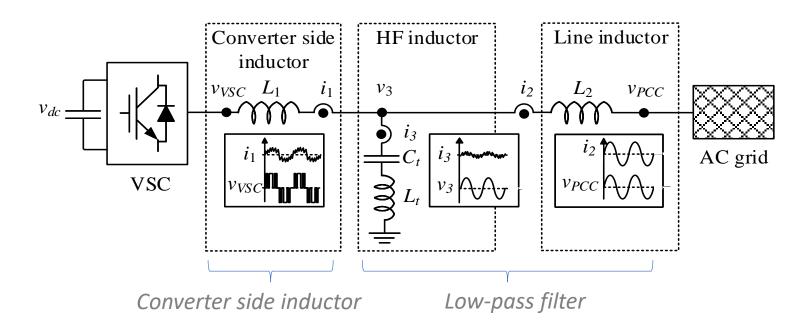
- Control loop also has to be included for $\omega_{opt} < \omega_{Nq}$
- Optimal selection results as a trade-off between filter resonance, damping losses and filter attenuation





Filter components [3]

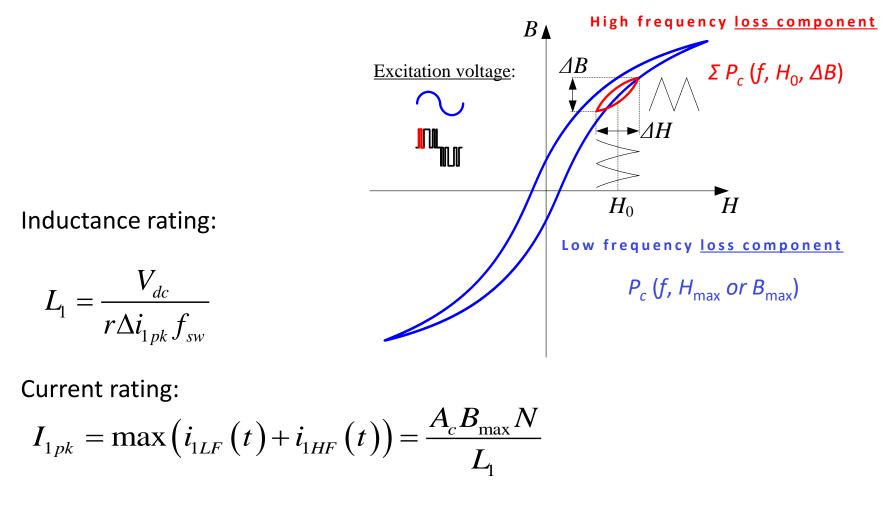
- Converter side inductor
- Shunt capacitor in series with switching frequency inductor
- Line inductor



[3] R. Beres, H. Matsumori, T. Shimizu, X. Wang, F. Blaabjerg and C. L. Bak, "Evaluation of Core Loss in Magnetic Materials Employed in Utility Grid AC Filters," in Proc. of the 31st Annual IEEE Applied Power Electronics Conference and Exposition, APEC 2016, pp. 3051-3057.



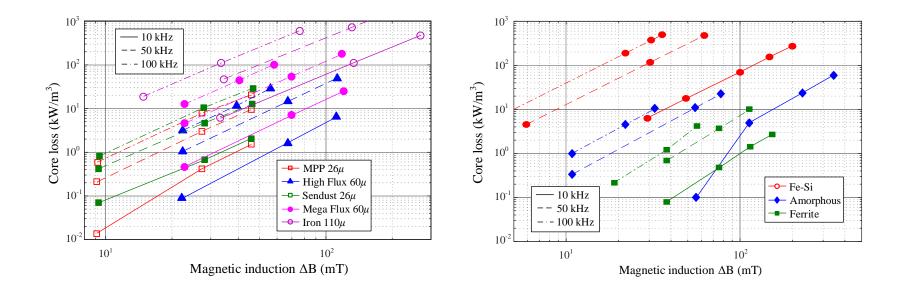
Design of the converter side inductor





High frequency loss component [3]

Rectangular voltage excitation, P_{cv} (f, $H_0=0$, ΔB)



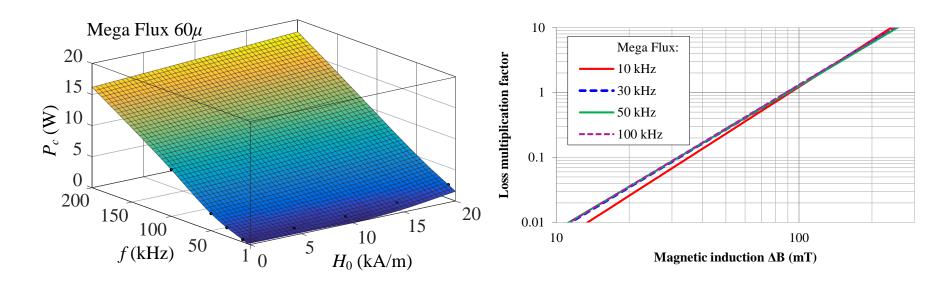
Total high frequency loss = ΣP_c (f, H₀, ΔB)!

[3] R. Beres, H. Matsumori, T. Shimizu, X. Wang, F. Blaabjerg and C. L. Bak, "Evaluation of Core Loss in Magnetic Materials Employed in Utility Grid AC Filters," in Proc. of the 31st Annual IEEE Applied Power Electronics Conference and Exposition, APEC 2016, pp. 3051-3057.



High frequency loss component

Rectangular voltage excitation, Loss map of P_{cv} (f, H_0 , $\Delta B = ct. = 0.09$ T) [6]



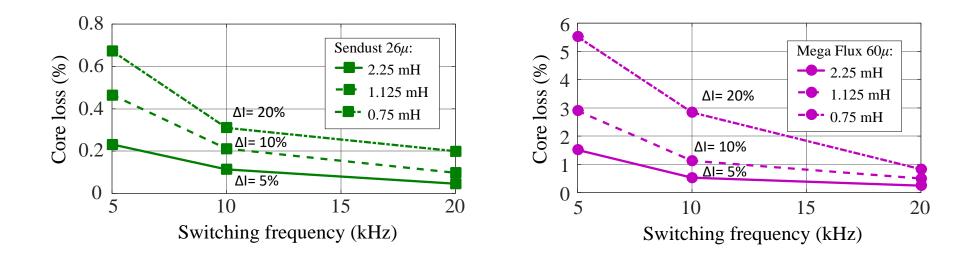
Total high frequency loss = ΣP_c (f, H₀, ΔB)!

[6] R. N. Beres "Optimal design of passive power filters for grid-connected voltage-source converters" PhD thesis, Aalborg University, 2016.



Core loss vs. current ripple [6]

Single phase inverter results for 5 %, 10 % and 20 % current ripple @ 5 Apk & @ 10 kHz, unipolar modulation with ma=0.9



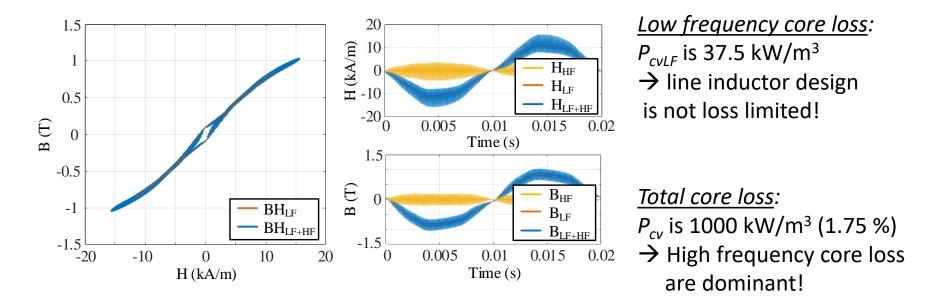
Total high frequency loss = ΣP_c (f, H₀, ΔB)!

[6] R. N. Beres "Optimal design of passive power filters for grid-connected voltage-source converters" PhD thesis, Aalborg University, 2016.



Actual operating waveforms and total core loss

Single phase inverter results with Mega Flux μ 60 converter side inductor with L=2.25 mH @ 10 A_{pk}, 5 kHz unipolar modulation and m_a=0.5:



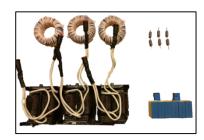
Total high frequency loss = ΣP_c (f, H₀, ΔB)!

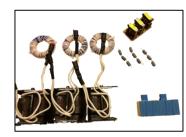
[6] R. N. Beres "Optimal design of passive power filters for grid-connected voltage-source converters" PhD thesis, Aalborg University, 2016.



Design example and loss evaluation of 10 kW - 10 kHz LCL filter [5]

Description	LCL+RC	Trap+RC	2 traps+RC
Total Losses (%)	1	1	0.95
Damping Losses (%)	0.075	0.071	0.053
THD _{vPCC} (%)	0.45	0.45	0.39
THD _{iPCC} (%)	1.27	1.12	2.67
i _{2(mf-2)} (%)	0.083	0.0083	0.0328
Volume (dm ³)	0.76	0.67	0.37
Magnetic materials	Fe-Si + Sendust	Fe-Si + Sendust + Ferrite	Sendust + Ferrite







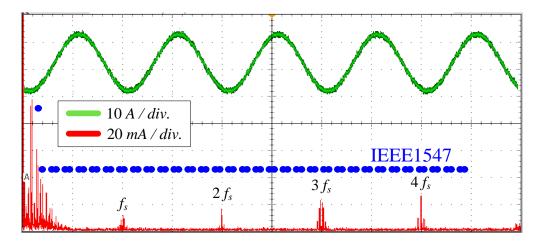
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[5] R. Beres, X. Wang, F. Blaabjerg, M. Liserre, and C. L. Bak, "Optimal Design of High-Order Passive-Damped Filters for Grid-Connected Applications," IEEE Trans. Power Electron., vol. 31, no. 3, 2016, pp. 2083–2098.



Current waveforms in 10 kW - 10 kHz LCL+2 traps filter [5]



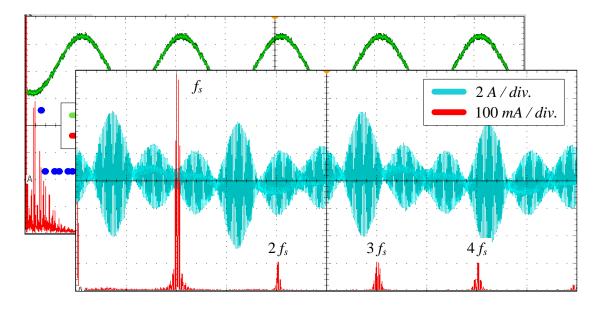
Grid current waveforms!



[5] R. Beres, X. Wang, F. Blaabjerg, M. Liserre, and C. L. Bak, "Optimal Design of High-Order Passive-Damped Filters for Grid-Connected Applications," IEEE Trans. Power Electron., vol. 31, no. 3, 2016, pp. 2083–2098.



Current waveforms in 10 kW - 10 kHz LCL+2 traps filter [5]



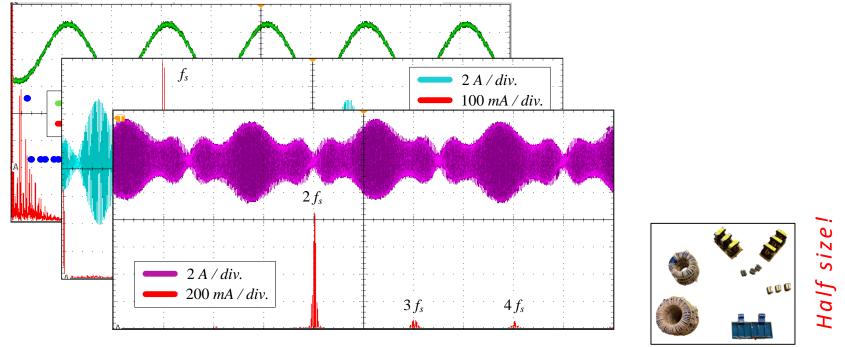
First trap current waveforms!



[5] R. Beres, X. Wang, F. Blaabjerg, M. Liserre, and C. L. Bak, "Optimal Design of High-Order Passive-Damped Filters for Grid-Connected Applications," IEEE Trans. Power Electron., vol. 31, no. 3, 2016, pp. 2083–2098.



Current waveforms in 10 kW - 10 kHz LCL+2 traps filter [5]

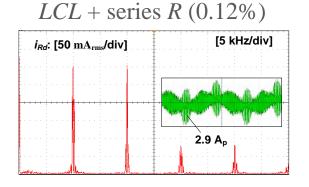


Second trap current waveforms!

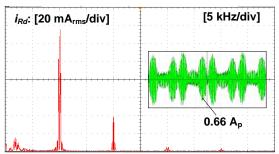
[5] R. Beres, X. Wang, F. Blaabjerg, M. Liserre, and C. L. Bak, "Optimal Design of High-Order Passive-Damped Filters for Grid-Connected Applications," IEEE Trans. Power Electron., vol. 31, no. 3, 2016, pp. 2083–2098.



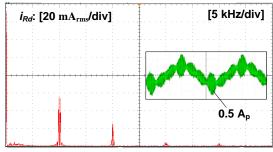
Damping loss evaluation in 10 kW - 10 kHz filter with different damping methods [4]



LCL + series RLC (0.01%)







Trap + shunt RC(0.03%)

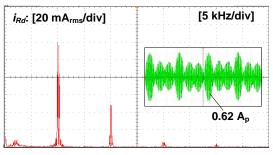
[5 kHz/div]

0.52 A_n

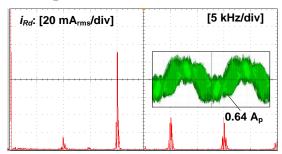
i_{Rd}: [20 mA_{rms}/div]

$LCL + \text{shunt } RC (0.045\%) \quad LCL + \text{shunt } RLC (0.028\%)$

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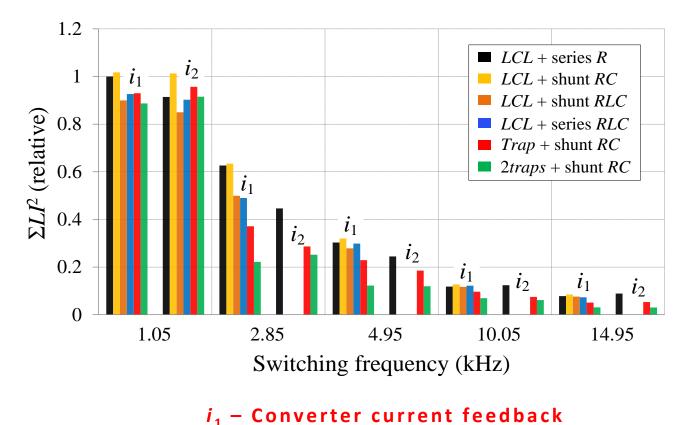
2Traps + shunt RC (0.03 %)



[4] R. Beres, X. Wang, M. Liserre, F. Blaabjerg, and C. L. Bak, "A Review of Passive Power Filters for Three Phase Grid Connected Voltage-Source Converters," IEEE Journal of Emerging and Selected Topics in Power Electronics, vol. 4, no. 1, 2016, pp. 54–69.



Filter size for different switching frequency and damping methods



 i_2 – Grid current feedback



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6. Conclusions

 The chosen magnetic material and current ripple level in the converter side inductance of the filter dictates on the <u>cost</u>, <u>size</u> and <u>efficiency</u> of the passive filter

- The harmonic current flow in the filter increases with decreasing the inductance and accurate damping is needed
- Accurate core loss for rectangular voltage excitation under dc-bias is also needed for accurate filter design and loss characterization
- Passive damped filters with RLC circuits in different configurations can be tuned to obtain very low damping losses over a wide range of operating conditions
- With tuned traps, the filter size can be reduced by up to 3 times depending on the switching frequency (at a price of increased component count and complexity)



7. Questions



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